

RJL60S5DPK-M0

600V - 20A - SJ MOS FET High Speed Power Switching R07DS0818EJ0002 Rev.0.02 Jun 21, 2012

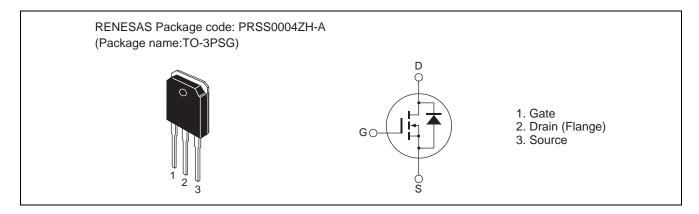
Features

- Superjunction MOSFET
- Built-in fast recovery diode
- Low on-resistance

 $R_{DS(on)}$ = 0.150 Ω typ. (at I_D = 10 A, V_{GS} = 10 V, Ta = 25°C)

• High speed switching

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	600	V
Gate to source voltage	V _{GSS}	(+30), (-20)	V
Drain current	I _D	20	А
Drain peak current	I _{D (pulse)} Note1	40	А
Body-drain diode reverse drain current	I _{DR}	20	А
Body-drain diode reverse drain peak current	I _{DR (pulse)} Note1	40	А
Channel dissipation	Pch Note2	192.3	W
Channel to case thermal impedance	θch-c	0.65	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. Limited by Tch max.

- 2. Value at Tc = 25°C
- 3. STch = 25° C, Tch $\leq 150^{\circ}$ C

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Electrical Characteristics

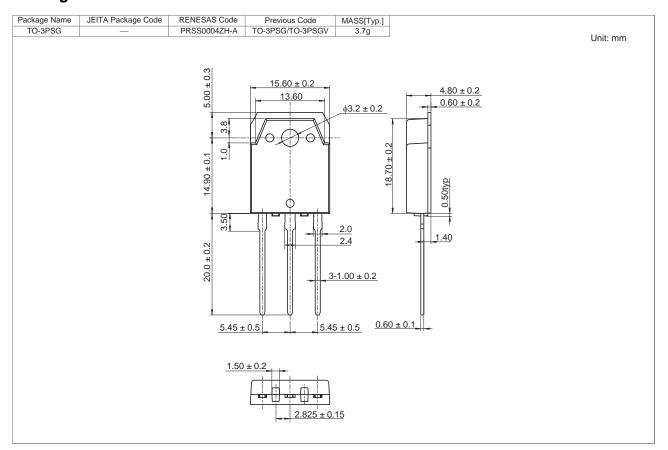
 $(Ta = 25^{\circ}C)$

Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	600	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Zero gate voltage drain current	I _{DSS}	_	_	1	mA	$V_{DS} = 600 \text{ V}, V_{GS} = 0$
Gate to source leak current	I _{GSS}	_	_	±0.1	μΑ	$V_{GS} = (+30 \text{ V}), (-20 \text{ V}), V_{DS} = 0$
Gate to source cutoff voltage	V _{GS(off)}	3	_	5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Static drain to source on state	R _{DS(on)}	_	0.150	0.178	Ω	$I_D = 10 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note4}}$
resistance						
Input capacitance	Ciss		1700	_	pF	V _{DS} = 25 V
Output capacitance	Coss	_	2050	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	13	_	pF	f = 100kHz
Body-drain diode forward voltage	V_{DF}	_	0.96	1.60	V	$I_F = 20 \text{ A}, V_{GS} = 0^{\text{Note4}}$
Body-drain diode reverse recovery time	t _{rr}	_	150	_	ns	I _F = 20 A, V _{GS} = 0
						di _F /dt = 100 A/μs

Notes: 4. Pulse test

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Package Dimension



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Renesas Electronics America Inc. 2880 Scott Boulevard Santa Clara, CA 95050-2554, U.S.A. Tel: +1-408-588-6000, Fax: +1-408-588-6130

Renesas Electronics Canada Limited 1101 Nicholson Road, Newmarket, Ontario L3Y 9C3, Canada Tel: +1-905-898-5441, Fax: +1-905-898-3220

Renesas Electronics Europe Limited
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K
Tel: +44-1628-585-100, Fax: +44-1628-585-900 Renesas Electronics Europe GmbH

Arcadiastrasse 10, 40472 Düsseldorf, Germany Tel: +49-211-65030, Fax: +49-211-6503-1327

Renesas Electronics (China) Co., Ltd. 7th Floor, Quantum Plaza, No.27 ZhiChunLu Ha Tel: +86-10-8235-1155, Fax: +86-10-8235-7679 nunLu Haidian District, Beijing 100083, P.R.China

Renesas Electronics (Shanghai) Co., Ltd.
Unit 204, 205, AZIA Center, No.1233 Lujiazui Ring Rd., Pudong District, Shanghai 200120, China Tel: +86-21-5877-1818, Fax: +86-21-6887-7858 / -7898

Renesas Electronics Hong Kong Limited
Unit 1601-1613, 16/F., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong
Tel: +852-2868-9318, Fax: +852 2869-9022/9044

Renesas Electronics Taiwan Co., Ltd. 13F, No. 363, Fu Shing North Road, Taipei, Taiwan Tel: +886-2-8175-9600, Fax: +886 2-8175-9670

Renesas Electronics Singapore Pte. Ltd. 1 harbourFront Avenue, #06-10, keppel Bay Tower, Singapore 098632 Tel: +65-6213-0200, Fax: +65-6278-8001

Renesas Electronics Malaysia Sdn.Bhd. เกลาเออออ Erectionius เพลาสู่ysta 3นที.bnu. Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No. 18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia Tel: +60-3-7955-9390, Fax: +60-3-7955-9510

Renesas Electronics Korea Co., Ltd. 11F., Samik Lavied' or Bldg., 720-2 Yeoksam-Dong, Kangnam-Ku, Seoul 135-080, Korea Tel: 482-2-588-3737, Fax: 482-2-588-5141

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